

09/826353
H1033U68 PRO

Class	Subclass
ISSUE CLASSIFICATION	

O.I.P.E.

PATENT DATE

SCANNER

Q.A

APPLICANTS

Naohiro Matsui

Phase capacitor, and phase locked loop circuit having the same as well as method of phase comparison

PTO-2040
12/99

ORIGINAL

CROSS REFERENCE(S)

CLASS

SUBCLASS

CLASS

SUBCLASS (ONE SUBCLASS PER BLOCK)

INTERNATIONAL CLASSIFICATION

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	 _____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	 _____ (Priamry Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	 _____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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